

Complementary MOSFET

ELM17600GA-S

General Description

ELM17600GA-S uses advanced trench technology to provide excellent $R_{ds(on)}$ and low gate charge. Internal ESD protection is included.

Features

- | | |
|---|--|
| N-channel | P-channel |
| • $V_{ds}=20V$ | $V_{ds}=-20V$ |
| • $I_d=0.9A(V_{gs}=4.5V)$ | $I_d=-0.6A(V_{gs}=-4.5V)$ |
| • $R_{ds(on)} < 300m\Omega (V_{gs}=4.5V)$ | $R_{ds(on)} < 550m\Omega (V_{gs}=-4.5V)$ |
| • $R_{ds(on)} < 350m\Omega (V_{gs}=2.5V)$ | $R_{ds(on)} < 700m\Omega (V_{gs}=-2.5V)$ |
| • $R_{ds(on)} < 450m\Omega (V_{gs}=1.8V)$ | $R_{ds(on)} < 950m\Omega (V_{gs}=-1.8V)$ |

Maximum Absolute Ratings

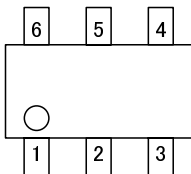
Parameter		Symbol	N-ch (Max.)	P-ch (Max.)	Unit	Note
Drain-source voltage		V_{ds}	20	-20	V	
Gate-source voltage		V_{gs}	± 8	± 8	V	
Continuous drain current	$T_a=25^\circ C$	I_d	0.90	-0.60	A	1
	$T_a=70^\circ C$		0.70	-0.48		
Pulsed drain current		I_{dm}	5	-3	A	2
Power dissipation	$T_a=25^\circ C$	P_d	0.30	0.30	W	
	$T_a=70^\circ C$		0.19	0.19		
Junction and storage temperature range		T_j, T_{stg}	-55 to 150	-55 to 150	$^\circ C$	

Thermal Characteristics

Parameter		Symbol	Device	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$t \leq 10s$	$R_{\theta ja}$	N-ch	360	415	$^\circ C/W$	1
	Steady-state			400	460	$^\circ C/W$	
Maximum junction-to-lead	Steady-state	$R_{\theta jl}$		300	350	$^\circ C/W$	3
Maximum junction-to-ambient	$t \leq 10s$	$R_{\theta ja}$	P-ch	360	415	$^\circ C/W$	1
	Steady-state			400	460	$^\circ C/W$	
Maximum junction-to-lead	Steady-state	$R_{\theta jl}$		300	350	$^\circ C/W$	3

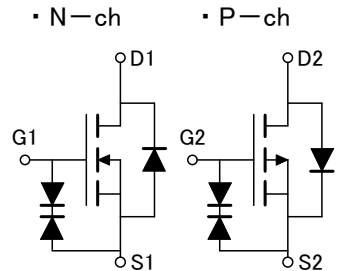
Pin Configuration

SC-70-6 (TOP VIEW)



Pin No.	Pin name
1	SOURCE1
2	GATE1
3	DRAIN2
4	SOURCE2
5	GATE2
6	DRAIN1

Circuit



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■ Electrical Characteristics (N-ch)

T_a=25°C

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	BV _{dss}	I _d =250 μA, V _{gs} =0V	20			V
Zero gate voltage drain current	I _{dss}	V _{ds} =16V V _{gs} =0V			1	μA
		T _j =55°C			5	
Gate-body leakage current	I _{gss}	V _{ds} =0V, V _{gs} =±8V			25	μA
Gate threshold voltage	V _{gs(th)}	V _{ds} =V _{gs} , I _d =250 μA	0.50	0.75	0.90	V
On state drain current	I _{d(on)}	V _{gs} =4.5V, V _{ds} =5V	5			A
Static drain-source on-resistance	R _{ds(on)}	V _{gs} =4.5V I _d =0.9A		181	300	mΩ
		T _j =125°C		253	330	
		V _{gs} =2.5V, I _d =0.75A		237	350	
		V _{gs} =1.8V, I _d =0.7A		317	450	
Forward transconductance	G _{fs}	V _{ds} =5V, I _d =0.8A		2.6		S
Diode forward voltage	V _{sd}	I _s =0.5A, V _{gs} =0V		0.69	1.00	V
Max.body-diode continuous current	I _s				0.4	A
DYNAMIC PARAMETERS						
Input capacitance	C _{iss}	V _{gs} =0V, V _{ds} =10V, f=1MHz		101	120	pF
Output capacitance	C _{oss}			17		pF
Reverse transfer capacitance	C _{rss}			14		pF
Gate resistance	R _g	V _{gs} =0V, V _{ds} =0V, f=1MHz		3	4	Ω
SWITCHING PARAMETERS						
Total gate charge	Q _g	V _{gs} =4.5V, V _{ds} =10V, I _d =0.8A		1.57	1.90	nC
Gate-source charge	Q _{gs}			0.13		nC
Gate-drain charge	Q _{gd}			0.36		nC
Turn-on delay time	t _{d(on)}			3.2		ns
Turn-on rise time	t _r		V _{gs} =5V, V _{ds} =10V, R _l =12.5 Ω		4.0	
Turn-off delay time	t _{d(off)}	R _{gen} =6 Ω		15.5		ns
Turn-off fall time	t _f			2.4		ns
Body-diode reverse recovery time	t _{rr}	I _f =0.8A, dI/dt=100A/μs		6.7	8.1	ns
Body-diode reverse recovery charge	Q _{rr}	I _f =0.8A, dI/dt=100A/μs		1.6		nC

NOTE :

1. The value of R_{θja} is measured with the device mounted on 1in² FR-4 board of 2oz. Copper, in still air environment with T_a=25°C. The value in any given applications depends on the user's specific board design. The current rating is based on the t_{≤10s} thermal resistance rating.
2. Repetitive rating, pulse width limited by junction temperature.
3. The R_{θja} is the sum of the thermal impedance from junction to lead R_{θjl} and lead to ambient.
4. The static characteristics in Figures 1 to 6 are obtained using 80 μs pulses, duty cycle 0.5%max.
5. These tests are performed with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_a=25°C. The SOA curve provides a single pulse rating.

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■ Typical Electrical and Thermal Characteristics (N-ch)

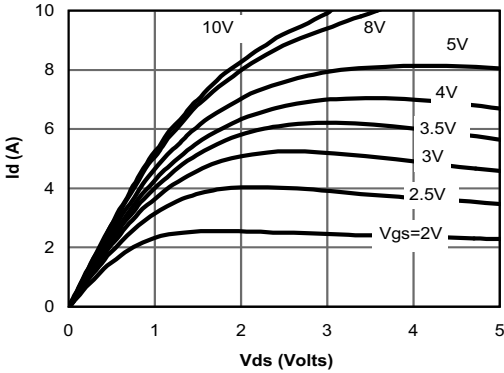


Fig 1: On-Region Characteristics

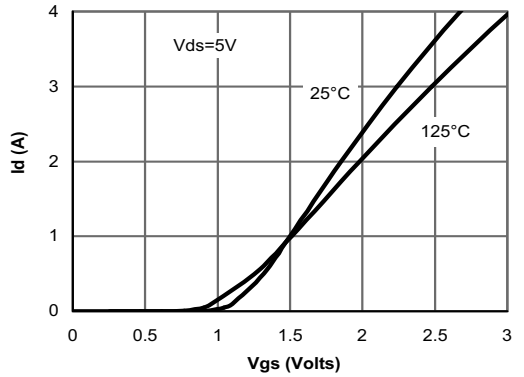


Figure 2: Transfer Characteristics

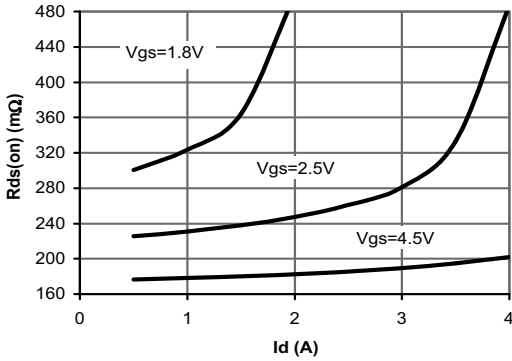


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

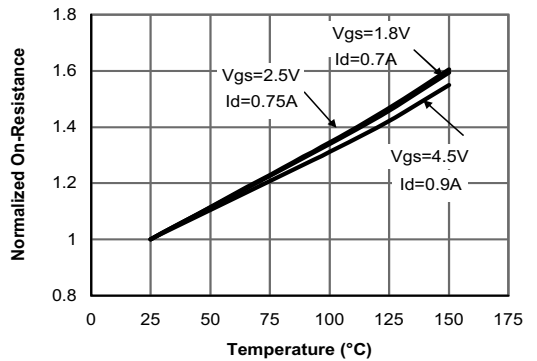


Figure 4: On-Resistance vs. Junction Temperature

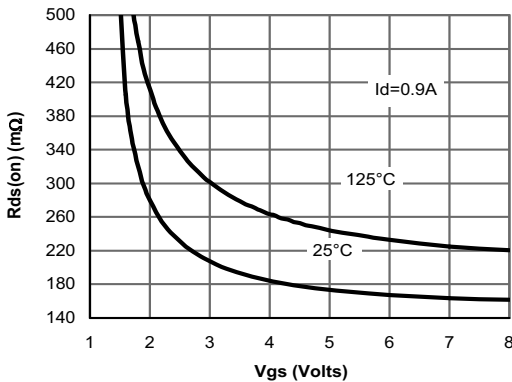


Figure 5: On-Resistance vs. Gate-Source Voltage

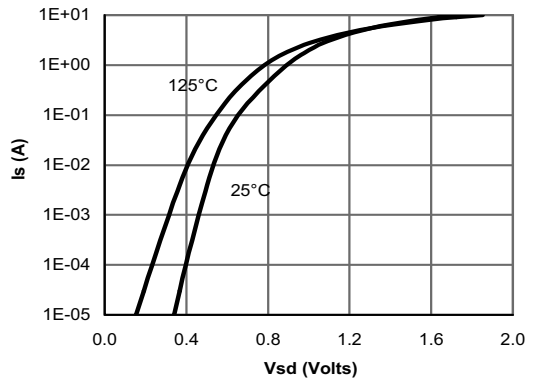


Figure 6: Body-Diode Characteristics

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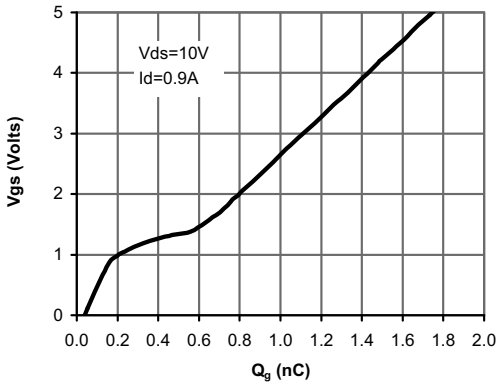


Figure 7: Gate-Charge Characteristics

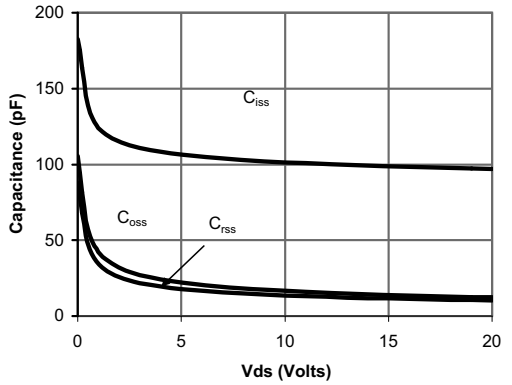


Figure 8: Capacitance Characteristics

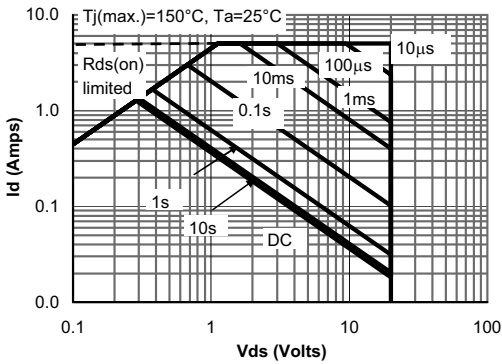


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

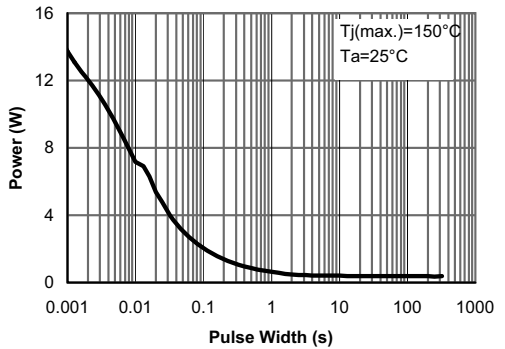


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

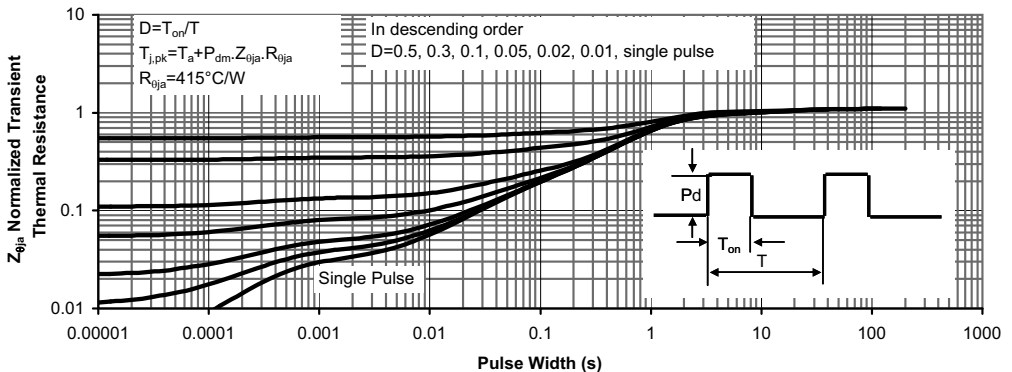


Figure 11: Normalized Maximum Transient Thermal Impedance

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Electrical Characteristics (P-ch)

T_a=25°C

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	BV _{dss}	I _d =-250 μA, V _{gs} =0V	-20			V
Zero gate voltage drain current	I _{dss}	V _{ds} =-16V			-1	μA
		V _{gs} =0V			-5	
		T _j =55°C				
Gate-body leakage current	I _{gss}	V _{ds} =0V, V _{gs} =±8V			±10	μA
Gate threshold voltage	V _{gs(th)}	V _{ds} =V _{gs} , I _d =-250 μA	-0.5	-0.6	-0.9	V
On state drain current	I _{d(on)}	V _{gs} =-4.5V, V _{ds} =-5V	-3			A
Static drain-source on-resistance	R _{ds(on)}	V _{gs} =-4.5V		415	550	mΩ
		I _d =-0.6A		542	700	
			T _j =125°C			
		V _{gs} =-2.5V, I _d =-0.5A		590	700	
		V _{gs} =-1.8V, I _d =-0.4A		700	950	
Forward transconductance	G _{fs}	V _{ds} =-5V, I _d =-0.6A		1.7		S
Diode forward voltage	V _{sd}	I _s =-0.5A, V _{gs} =0V		-0.86	-1.00	V
Max. body-diode continuous current	I _s				-0.4	A
DYNAMIC PARAMETERS						
Input capacitance	C _{iss}			114	140	pF
Output capacitance	C _{oss}	V _{gs} =0V, V _{ds} =-10V, f=1MHz		17		pF
Reverse transfer capacitance	C _{rss}			14		pF
Gate resistance	R _g	V _{gs} =0V, V _{ds} =0V, f=1MHz		12	17	Ω
SWITCHING PARAMETERS						
Total gate charge	Q _g	V _{gs} =-4.5V, V _{ds} =-10V		1.44	1.80	nC
Gate-source charge	Q _{gs}	I _d =-0.6A		0.14		nC
Gate-drain charge	Q _{gd}			0.35		nC
Turn-on delay time	t _{d(on)}	V _{gs} =-4.5V, V _{ds} =-10V R _l =16.7 Ω, R _{gen} =3 Ω		6.5		ns
Turn-on rise time	t _r			6.5		ns
Turn-off delay time	t _{d(off)}			18.2		ns
Turn-off fall time	t _f			5.5		ns
Body diode reverse recovery time	t _{rr}	I _f =-0.6A, dI/dt=100A/μs		10	13	ns
Body diode reverse recovery charge	Q _{rr}	I _f =-0.6A, dI/dt=100A/μs		3		nC

NOTE :

1. The value of R_{θja} is measured with the device mounted on 1in² FR-4 board of 2oz. Copper, in still air environment with T_a=25°C. The value in any given applications depends on the user's specific board design. The current rating is based on the t_{≤10s} thermal resistance rating.
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Typical Electrical and Thermal Characteristics (P-ch)

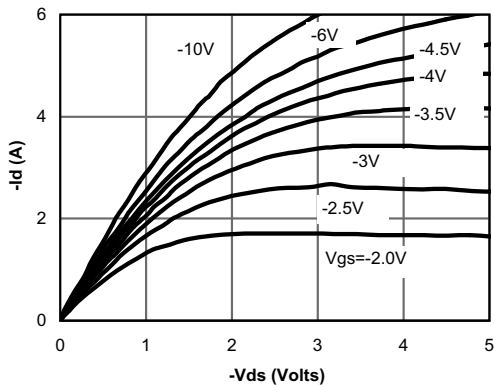


Fig 1: On-Region Characteristics

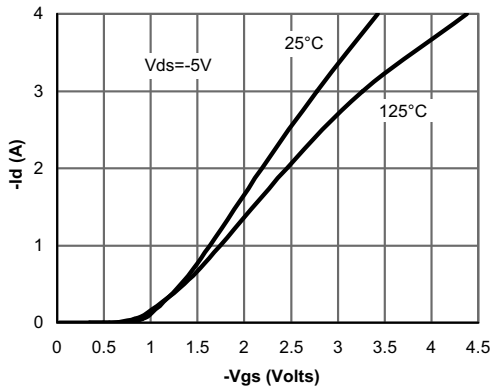


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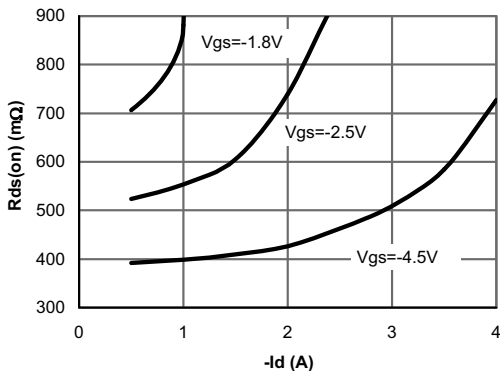


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

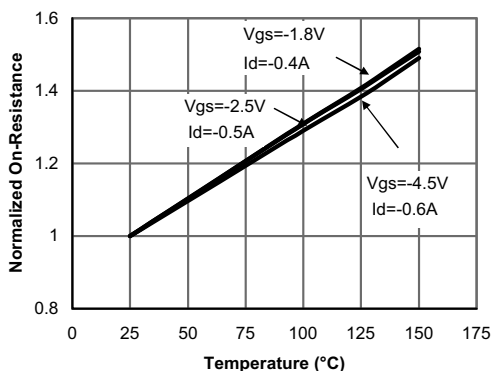


Figure 4: On-Resistance vs. Junction Temperature

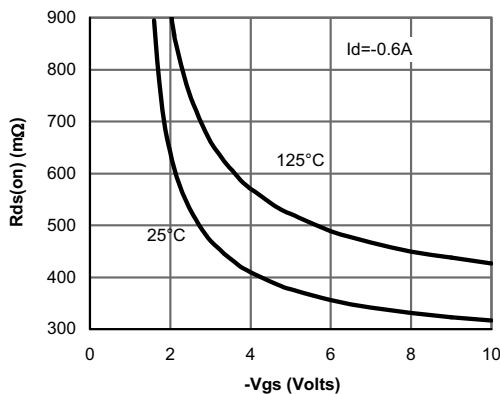


Figure 5: On-Resistance vs. Gate-Source Voltage

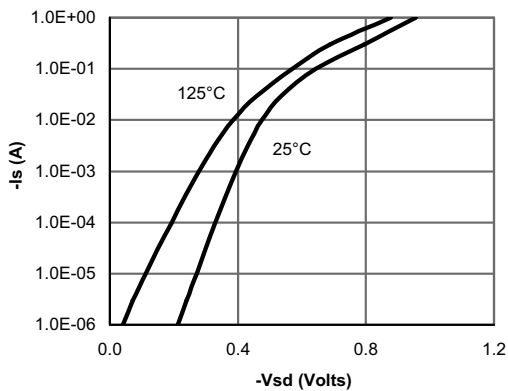


Figure 6: Body-Diode Characteristics

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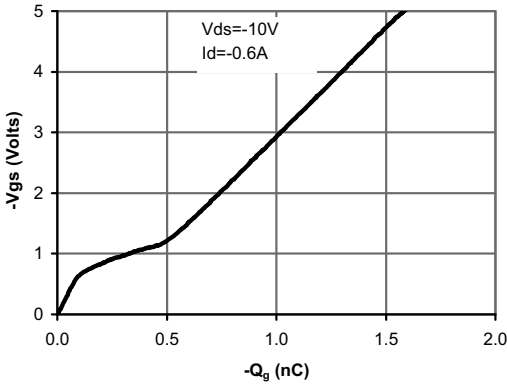


Figure 7: Gate-Charge Characteristics

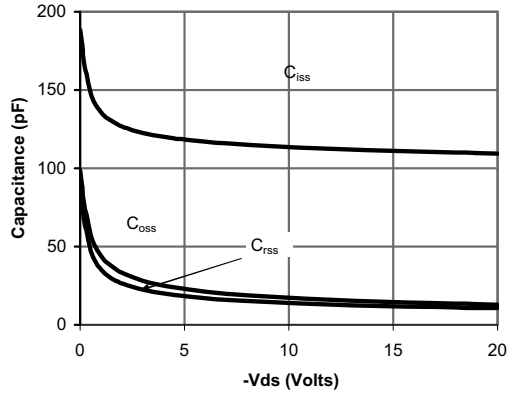


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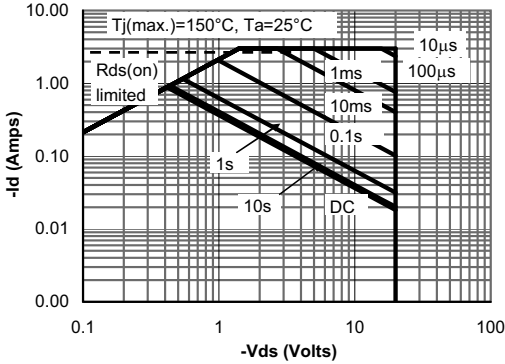


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

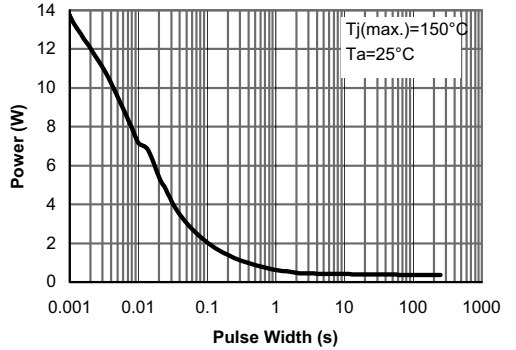


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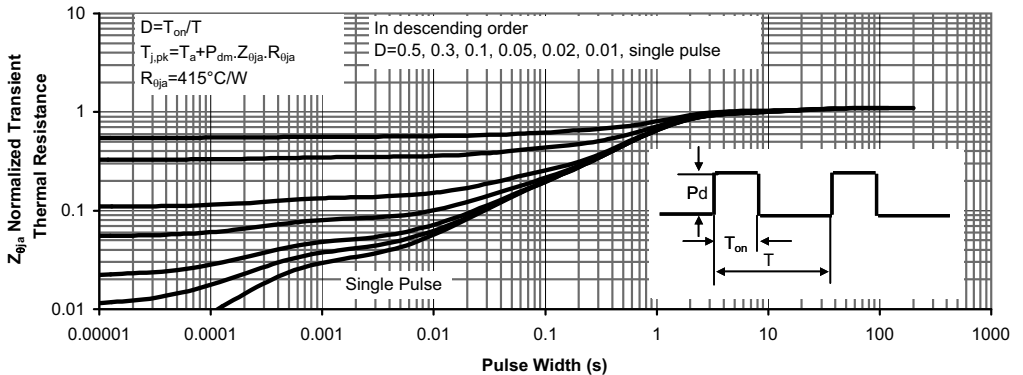


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